

Amendments to the Specification

After page 1 line 1 please insert the following heading:

a<sup>1</sup> FIELD OF THE INVENTION

After page 1 line 7 please insert the following heading:

a<sup>2</sup> BACKGROUND OF THE INVENTION

After page 2 line 4 please insert the following heading:

a<sup>3</sup> SUMMARY OF THE INVENTION

Please delete the paragraph beginning at page 2 lines ~~7-11~~ and insert the following new paragraphs in its place:

a<sup>4</sup> Accordingly, the present invention provides multi-channel image display apparatus comprising at least two low frame rate source channels for forming a background scene, at least one high frame rate source channel, and high speed real-time image processing electronic means which enables an image from the high frame rate source channel to be inserted at any location in the background scene.

The frame rates from the high frame rate source channel and the low frame rate source channels may be synchronous. The high frame rate source channel may provide partial frames. The partial frames from the high frame rate source channel may form a target image. There may be more than one of the target images. In an alternative embodiment, the partial frames from the high frame rate source channel may form an area of interest.

Please delete the paragraph at page 2 lines ~~24-26~~.

Please delete the paragraph at page 3, lines ~~1-2~~.

Please replace the paragraph at page 3, lines ~~3-7~~ with the following rewritten paragraph:

a<sup>5</sup> The invention further provides a simulator when including the display apparatus. The simulator may be a flight simulator, an air traffic control simulator, a driving simulator, or any other suitable and appropriate desired simulator.

After page 4 line 12 please insert the following heading:

a6  
BRIEF DESCRIPTION OF THE DRAWINGS

After page 5 line 7 please insert the following heading:

a7  
DETAILED DESCRIPTION OF THE DRAWINGS

Please replace the paragraph at page 13, lines 11-25 with the following paragraph:

a8  
Figure 8 is similar to Figure 7 but shows a specific example as will be seen. In Figure 8, the section 28 has a display system comprising four output channels as shown from combined background, and target channels running at different frame rates. In this example, the target images are ~~[[partical]]~~ partial frame segments and the image processing electronics may insert any one or more of these into the scenes provided by the background channels. Hence at the video signal to any one display channel, there may be image data that updates at varying rates where part or parts of the image is updated at a higher rate than the remainder. The video switching function therefore operates at sub-frame level, where individual display image data is selected from sources that may update at different but synchronous rates.